

**REMARKS/ARGUMENTS**

Various claims are being amended as shown above. The claim amendments clarify the claim language and are not intended to limit the scope of the claims, unless the claim language is expressly quoted in the following remarks to distinguish over the cited art. No new matter is being added by virtue of the amendment to the claims.

In the previous final office action, claims 13, 16 and 18-20 were rejected under 35 U.S.C. 102(b) as allegedly being anticipated by Jodoin (US Pat. No. 4,638,918). Applicant respectfully traverses the rejection.

Jodoin is directed to an integrated circuit 10" and capacitor 20" both mounted on a printed circuit board 14", where the leads 28" of the capacitor 20" will shared the same through-holes 16" (in the board 14") as the ground/power leads of the integrated circuit 10". However, Jodoin does not disclose the dielectric layers and ground and power planes, as recited in claim 13. Accordingly, claim 13 is patentable over Jodoin.

For the above reasons, Applicant requests reconsideration and withdrawal of the previous rejection under 35 U.S.C. §102.

In the office action, claims 13 and 16-20 were rejected under 35 U.S.C. 102(e) as allegedly being anticipated by Kozak (US Pat. No. 6,414,850). Applicant respectfully traverses the rejection.

Kozak is directed to a main circuit board 114 with a capacitor 412 having capacitance and with a mounted integrated circuit package 112. However, Kozak does not disclose a ground plane and power plane that are connected to a first contact pad and a second contact pad,

respectively, as recited in claim 13. Accordingly, claim 13 is patentable over Kozak.

For the above reasons, Applicant requests reconsideration and withdrawal of the previous rejection under 35 U.S.C. §102.

In the previous office action, claims 14-15 were rejected under 35 U.S.C. 103(a) as allegedly being unpatentable over Kozak or Jodoin in view of Kabadi (U.S. Patent No. 6,097,609), and further in view of Wisser (U.S. Patent No. 3,721,941). Applicant respectfully traverses the rejection.

The Examiner correctly admits in the previous final office action that Kozak and Jodoin do not specifically disclose a first interposer (or socket) between a component and a first electrical contact area on a first side of a substrate and a second interposer (or socket) between a capacitor plate and a second electrical contact area on a second side of the substrate. In an attempt to overcome the deficiencies of Kozak and Jodoin, the Examiner relies on Kabadi and Wisser in an attempt to show various features.

Claims 14-15 each depends from claim 13 and are each patentable over the Kozak-Kabadi-Wisser combination or the Jodoin-Kabadi-Wisser combination for at least the same reasons that claim 13 is patentable over the cited references, considered singly or in combination. Furthermore, each of the claims 14-15 distinguishes over the Kozak-Kabadi-Wisser combination and the Jodoin-Kabadi-Wisser combination by reciting additional features in combination with the features recited in the respective base claim. Accordingly, claims 14-15 are each patentable

over the Kozak-Kabadi-Wisser combination and the Jodoin-Kabadi-Wisser combination.

For the above reasons, Applicant requests reconsideration and withdrawal of the previous rejection under 35 U.S.C. §103.


Accordingly, Applicant respectfully requests allowance of all pending claims.

**CONTACT INFORMATION**

If the Examiner has any questions or needs any additional information, the Examiner is invited to telephone the undersigned attorney at (805)681-5078.

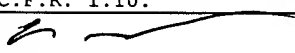
Date: April 25, 2007 ,

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